NATIONAL INSTITUTE OF TECHNOLOGY ANDHRA PRADESH
Department of Electronics and Communication Engineering

PRESENTS
Five Days Online Faculty Development Programme (FDP) on
Multifaceted Developments in SIGNAL PROCESSING (md-SP)

5TH - 9TH OCTOBER 2020

Patron
PROF. CH. SURYA PRAKASH RAO

Coordinators
Dr. P. Kishore Kumar
Dr. G. Kiran Kumar

Other Speakers
Dr. Pratik
Dr. Ramesh Kumar B
Dr. Songhita Misra

Last date for applying: 30th Sept. 2020
Shortlisted candidates will be intimated through email on 03rd Oct. 2020

Registration Fee
Faculty Members/Industry Professionals: Rs. 500
Ph.D. Scholars/P.G. Students/Others: Rs. 300
Registration fee can be paid through any online mode to the A/c No: 34999496394, A/c Name: Director NIT Andhra Pradesh, IFSC CODE: SBIN0016305, State Bank of India, Satyavathinagar Branch, Tadepalligudem-534102.

CLICK HERE TO APPLY

Objective of md-SP 2020: To keep abreast of the recent developments in signal processing

National Institute of Technology, Andhra Pradesh is the 31st institution among the chain of NITs started by the Government of India and is established in the state of Andhra Pradesh in the academic year 2015 – 2016. With the support of Dept. of Education (formerly MHRD) and an active role of the present director Prof. C. S. P. Rao, the institute started functioning from its permanent campus which is spread across 178 acres by the side of NH.16 near Tadepalligudem. The institute offers B.Tech, M.Tech, M.S. (by research) and Ph.D. programmes in eight engineering disciplines.

Department of Electronics and Communication Engineering is one of the pioneering departments of the Institute which is established in the year of 2015 with an intake of 90 UG students. The department has been successful in producing excellent and well-trained graduates. In addition to the B.Tech, the department at present offers M. Tech in Advanced Communication Systems and Signal Processing, M.S (by Research) and Ph.D. programmes. The department has qualified, dedicated, experienced faculty members whose research areas includes Signal Processing, Communications, Embedded Systems, Microwaves and VLSI.

General Instructions
md-SP FDP will be conducted in online mode and the link to participate in this will be circulated to the shortlisted candidates through email. This FDP is conducted in two sessions per day. Morning 1000 hrs. – 1200 hrs. and after noon 1430 hrs. to 1630 hrs. An e-certificate will be issued to all the participants who attend these sessions. As this FDP is conducted through online mode, there is a maximum limit for the number of participants and hence the selection will be done on first cum - first serve basis. The current research challenges in the signal processing along with few demonstrations will be addressed in this md-SP. This may be useful for the participants who are interested in doing the research in this area, who are pursuing Ph.D. and are going to identify the research gap in signal processing.