



# NATIONAL INSTITUTE OF TECHNOLOGY ANDHRA PRADESH

(An Institute of National Importance, under the aegis of Ministry of Education, Government of India)  
TADEPALLIGUDEM- 534102, WEST GODAVARI DIST., ANDHRA PRADESH, INDIA.

Department of Electrical Engineering

Ref. No. NITANP/DEEN/TR/SERB-EMEQ/JRF/2025/

Date: 09.01.2025

## **Advertisement for the Position of Junior Research Fellow under the SERB Project (EEQ/2022/001043)**

Applications are invited from the interested candidates for the position of **Junior Research Fellow (JRF)** to work on the R&D project under Department of Science and Technology (DST) - Science & Engineering Research Board (SERB), **Empowerment and Equity Opportunities for Excellence in Science (EMEQ)** scheme, Govt. of India, sponsored project bearing the sanction number EEQ/2022/001043 undertaken in the Department of Electrical Engineering, NIT Andhra Pradesh.

S. No.	Particulars	:	Details
1.	Name of the Temporary Post	:	Junior Research Fellow (JRF)
2.	Name of the Research Project	:	Design and Development of Multi-level Inverter Fed Multi-Phase Sensorless Permanent Magnet Synchronous Motor Drive for Hybrid Electric Vehicle Applications with Reduced Torque and Flux Ripples.
3.	Name of the Sponsoring Agency	:	Science and Engineering Research Board (SERB), Govt. of India.
4.	Tenure of the Project	:	12 Months
	Tenure of Assignment	:	The initial appointment will be for one year, which is extendable up to the duration of the project, solely based on satisfactory performance
5.	No. of positions	:	01 (ONE)
6.	Consolidated monthly compensation/ Fellowship	:	Rs. 31,000/- pm plus House Rent Allowance (HRA) as per the Institute rules
7.	Principal Investigator	:	Dr. Tejavathu Ramesh
8.	Department	:	Electrical Engineering
9.	Essential Qualifications	:	B.E/B.Tech in Electrical Engineering/Electrical and Electronics Engineering/Instrumentation and M.E/M.Tech. in relevant specializations of Electrical Engineering/ Electrical and Electronics Engineering/Instrumentation with 60% aggregate marks (or a minimum CGPA of 6.5) either in UG or PG level for UR/EWS/OBC-NCL category

			<b>with a GATE/NET Qualified.</b> For SC/ST/PwD candidates, 55% aggregate (or above) marks or equivalent CGPA of 6.0 <b>with a GATE/NET Qualified.</b>
10.	Desirable Qualifications/ Experiences	:	Analytical and experimental activities during M.E/M.Tech. for the development of power electronics based system with concluding understanding, knowledge and experience with technical software/tools (MATLAB/SIMULINK, PSIM, SNSYS, etc.) for simulation studies and dSPACE/FPGA/Micro/DSP controllers for the implementation of control techniques. <b>Candidate is expected to join immediately.</b>
11.	Job Description	:	Research project implementation, documentation and whatever needed to execute the project successfully
12.	Accommodation	:	The selected candidate may be provided on-campus hostel accommodation & mess facilities subject to availability
13.	Other Benefits	:	The selected candidate is eligible to register for Ph.D. degree at NIT Andhra Pradesh as per institute rules. However, s(he) needs to apply for Ph.D. position when the advertisement is open.
14.	<b>Submission mode</b>		<b>soft copy</b> (The proforma of application is attached with this advertisement. Complete and send along with educational certificates that to an <b>email ID: tramesh.ee@nitandhra.ac.in</b>
15.	Last date for receipt of application	:	30.01.2025
16.	<b>Date and Time for the Interview</b>	:	<b>05.02.2025 at 10.00 AM (Tentative)</b>
17.	Venue**	:	Dean, R&C office, Administrative Vista, National Institute of Technology Andhra Pradesh, Tadepalligudem- 534101, Andhra Pradesh, India
18.	Documents Required	:	Applicant should bring all original certificates including mark sheets (matriculation onwards), gate/net qualifications, experience certificate(s), monthly emoluments/salary drawn, publications and other relevant documents (including one set of self-attested certificates, updated CV and filled-in application form).

**\*\* (Mode of Interview will be Intimated to the candidates through Email)**

#### **General Instructions to the applicants:**

1. Selected candidates shall not have claim on any regular position and shall not be bestowed any of the privileges like Housing, Medical Facility and Other benefits available to regular employees of the Institute.
  2. Applicants are advised to ensure, before appearing for Selection Process, that they possess the minimum essential qualification and experience laid down for the post. Qualification obtained
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has to be from recognized University/Institute. Eligibility in terms of age, qualification and experience of a candidate shall be considered as on the last date for receipt of application.

- 3. No TA/DA will be paid for attending the Interview.**
4. In case of any inadvertent mistake in the process of selection, which may be detected at any stage even after issue of the appointment letter, the Institute reserves the right to modify, withdraw or cancel any communication made to the Applicants.
5. After joining the service of the Institute, the person will have to abide by the Rules, Regulations, Ordinances, Statutes and Act of the Institute applicable from time to time. He/She may be assigned any duty within or outside the Institute depending upon the exigency of the work.
6. The Institute shall verify the antecedents or documents submitted by a person at the time of appointment or during the tenure of the service. In case it is detected that the documents submitted by the candidates are fake, or the person has a clandestine antecedents or background and has suppressed the said information, then his/her services shall be terminated forthwith and legal action may be initiated against such candidates/employees.
- 7. Canvassing in any form or bringing in any influence political or otherwise, will be a disqualification for the post.**
8. In case of any dispute/ambiguity that may occur in the process of selection, the decision of the Institute shall be final.
9. **Candidates shall build a Single PDF file** (filled-in application form along with supporting documents); **it is to be sent through Email to** Principal Investigator Dr. Tejavathu Ramesh, Assistant Professor, Department of Electrical Engineering, NIT Andhra Pradesh, email ID: [tramesh.ee@nitandhra.ac.in](mailto:tramesh.ee@nitandhra.ac.in) **on or before 30.01.2025, 23:59 HRS.** The candidates are also required to produce relevant documents mentioned in the application form (such as original of all mark sheets and certificates, research papers (if any), experience certificate (if any) etc.) at the time of interview. Selection / Joining will be canceled in case of any suppression of information / document provided earlier. No hard copies of the application will be accepted. **Only the shortlisted candidates will be informed by Email.**
10. Mere possession of minimum qualification does not guarantee invitation to the interview. Candidates will be short listed based on merit and need of the project. Only the shortlisted candidates will be informed by E-mail.

For technical information on the project, the candidate may contact the Principal Investigator at the following address:

Dr. Tejavathu Ramesh  
Assistant Professor  
Department of Electrical Engineering,  
National Institute of Technology Andhra Pradesh,  
Tadepalligudem – 534 102, West Godavari Dist.,  
Andhra Pradesh, India.  
Email: [tramesh.ee@nitandhra.ac.in](mailto:tramesh.ee@nitandhra.ac.in)  
Phone. No: 9034003377.

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Department of Electrical Engineering

## Application for the Position of JRF

DST-SERB-EMEQ Sponsored Project: *Design and Development of Multi-level Inverter Fed Multi-Phase Sensorless Permanent Magnet Synchronous Motor Drive for Hybrid Electric Vehicle Applications with Reduced Torque and Flux Ripples.*

**Last Date of Application: 30.01.2025**

1. Name of the Candidate (In BLOCK LETTERS):

\_\_\_\_\_

2. Date of Birth (DOB): \_\_\_\_\_

3. Age in Years as on Closing Date of Application (YY:MM): \_\_\_\_\_

4. Sex (M/F): \_\_\_\_\_

5. Marital Status (Single/Married): \_\_\_\_\_

6. Category (UR/OBC/SC/ST): \_\_\_\_\_

7. Nationality: \_\_\_\_\_

8. Name of the Post applied for: \_\_\_\_\_

9. Mobile No.: \_\_\_\_\_

10. Email Address: \_\_\_\_\_

11. Address:

Correspondence: \_\_\_\_\_

\_\_\_\_\_

\_\_\_\_\_

Permanent: \_\_\_\_\_

\_\_\_\_\_

\_\_\_\_\_

Passport size  
**Recent**  
Photograph

12. Academic/Professional Qualifications (Attach the copy of proof, mark sheet/grade card, etc):

<b>Educational Qualifications</b>	<b>Institute/ University/ Board</b>	<b>Discipline/ Field</b>	<b>Year of Passing</b>	<b>Class/ Division</b>	<b>% of Marks/ CGPA</b>
M. Tech./M.E					
B. Tech./B.E					
12 <sup>th</sup> Standard					
SSC/ 10th					

13. GATE/NET Qualification (Yes/No):

Appeared Year:

GATE/NET Rank:

GATE/NET Score:

14. Details of work Experience if any: (Enclose the copy of experience certificate and/or NOC)

15. Previous Research work and Publications if any (enclose first page of publication):

### **DECLARATION**

I \_\_\_\_\_ hereby declare that all the statements made in this application are true, complete and correct to the best of my knowledge and belief. In the event of the information being found false or incorrect or any ineligibility being detected before or after the selection, my candidature is liable to be cancelled.

Date:

Signature of the Applicant

Place:

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